

RTL8111E/RTL8105E 48Pin Reference Schematic V104

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Realtek Semiconductor Corp.		
No.2, Industry E. Rd. IX, Science-Based Industrial Park, Hsinchu, Taiwan 300		
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E

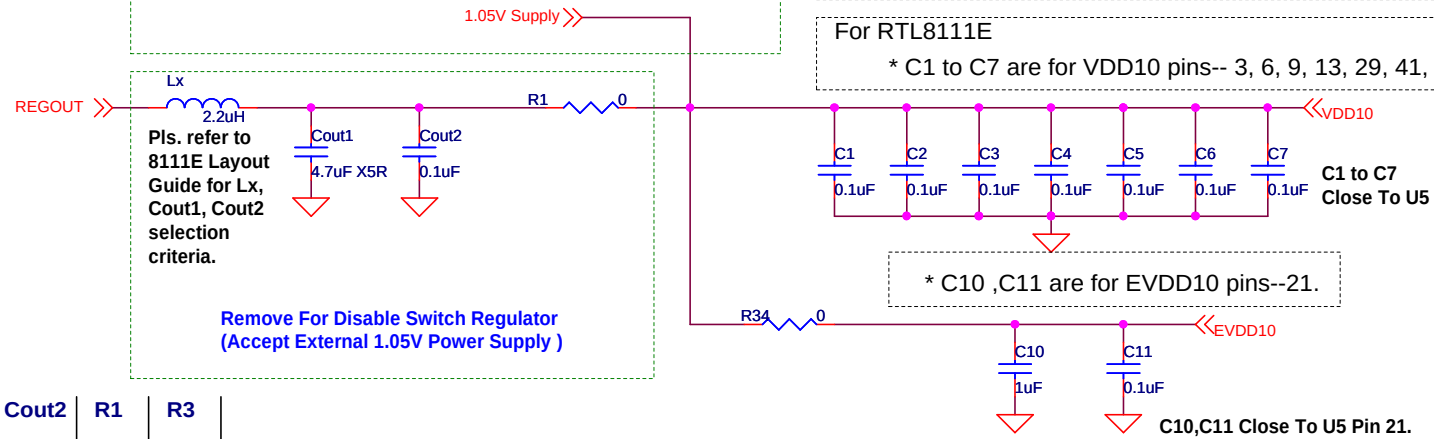
Use External 1.05V Supply When Disable Switch Regulator.
If Using External 1.2V Supply Pls. Contact With FAE.

For RTL8105E

* C1 to C4 are for VDD10 pins-- 3, 13, 29, 45.

For RTL8111E

* C1 to C7 are for VDD10 pins-- 3, 6, 9, 13, 29, 41, 45.



	Lx	Cout1	Cout2	R1	R3
Enable Switch Regulator	O	O	O	O	O
Disable Switch Regulator	X	X	X	X	X

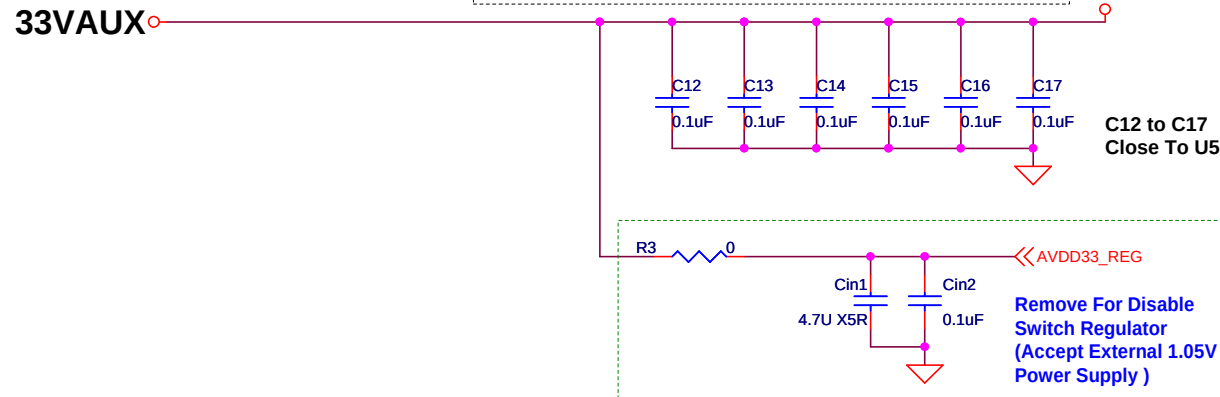
For RTL8105E

* C12 to C16 are for VDD33 pins-- 27, 39, 42, 47, 48.

For RTL8111E

* C12 to C17 are for VDD33 pins-- 12, 27, 39, 42, 47, 48.

VDD33



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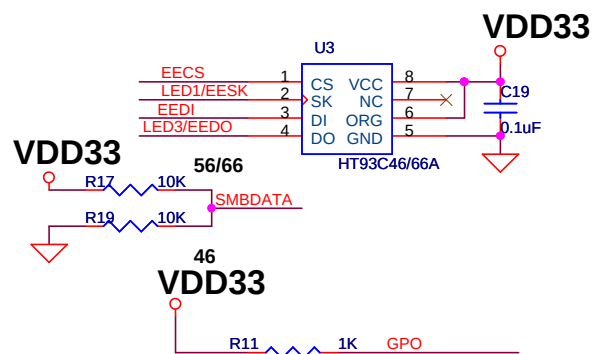
Title
RTL81111E/8105E Reference circuit: Power

Size A4 Document Number
RTL81111E/8105E Reference circuit

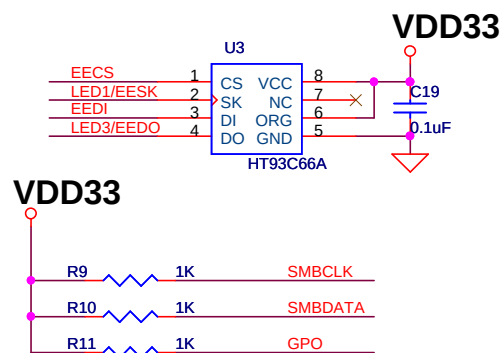
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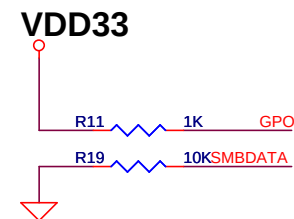
For RTL8111E EEPROM / ASF / Efuse Function (3 Option)



1. When using EEPROM only without ASF function.
Using R17 For 93C56/66
Using R19 For 93C46

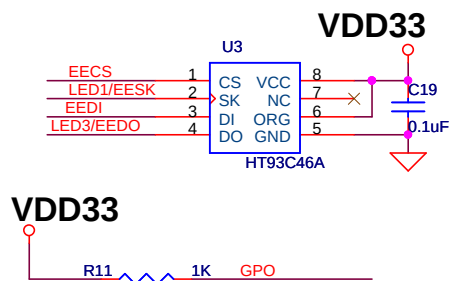


2. When Using EEPROM(Only 93C56/66) With ASF Function



3. When using EFuse/BIOS Patch without ASF function.

For RTL8105E EEPROM / Efuse Function (2 Option)



1. When using EEPROM(93C46A)



2. When using EFuse/BIOS Patch.

Change Notice from 1.00 to 1.01

1. C17 rename to Cin1, C18 rename to Cin2. C8 rename to Cout1, C9 rename to Cout2, L1 rename to Lx to align with SWR Layout guide (Page 2)
2. Add supported resistor values for Dual Color LED , supported combinations when using EEPROM (Page 3)
3. Add R38 for external clock circuit. (Page 3)

Change Notice from 1.01 to 1.02

1. Update Pin name(Pin3 , Pin47) For RTL8105E.(Page 3.)
2. Update Capacitor Description For RTL8105E and RTL8111E. (Page 2.)
3. Modify Center tap Schematic for Power Optimization. Center tap short and coconnector 1uF to GND.(Page 3.)
4. Add RTL8105 Optional Function Schematic.(Page 4.)

Change Notice from 1.02 to 1.03

1. Modify Efuse Autoload Schematic ,SMBData Pull Down. (Page 4.)

Change Notice from 1.03 to 1.04

1. Remove TWSI Schematic. (Page 3 & 4.)
2. Remove TWSI Share Pin Name. (Page 3 & 4.)
3. Add Note For Lanwakeb & Clkreqb.(Page 3)
3. Add Note For Function Option for Using Efuse/BIOS Patch.(Page 4)

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